

Amendment to the Abstract

Please amend the abstract of the disclosure as follows:

A system and method are used to perform division or complex division using multiplication and/or summation devices and steps. A numerator and denominator of a complex division signal are filtered. A separate determination of their values is performed using separate logic systems. The separate values are multiplied together [[for]] to form an output signal. The denominator logic system converts the complex division signal into a signal that is processed using multiplication and summation devices. The processing adjusts an approximated past value using an error value. The error value can be based on a present value, a past value, and a scaling coefficient.